

REMARKS

Claims 1 and 3-19 are pending in this application. By this Amendment, claims 1, 3, 5, 6 and 9 are amended. Claims 14-18 have been withdrawn by the Examiner.

I. Allowable Subject Matter

Applicant appreciates the Office Action's acknowledgement of allowable subject matter in claim 12. As explained below, it is respectfully submitted that claim 12 is allowable.

II. Objection to the Drawings

The drawings are objected to under 37 CFR 1.83(a) because they purportedly do not show the capacitor electrode wiring lines. The objection is respectfully traversed.

The Office Action asserts that the drawings show only one capacitor electrode wiring line and thus do not support the recitation of capacitor electrode wiring lines. It is respectfully submitted that the drawings, such as Fig. 1, are a schematic wiring diagram illustrating an arrangement of elements and wiring lines. It is further submitted that a person of ordinary skill would recognize that a single line, such as capacitor electrode wiring line 503, in a schematic wiring diagram can illustrate both a single wiring line or a plurality of wiring lines arranged and electrically connected to elements as shown. Accordingly, it is respectfully submitted that at least Fig. 1 discloses more than one capacitor electrode wiring line in the schematic of capacitor electrical wiring line 503, as the schematic can represent multiple wiring lines (electrically connected as a single line) when the schematic is interpreted and applied to a real-world electro-optical device. It is respectfully requested that the objection be withdrawn.

III. Claim Objections

Claims 1, 3, 5, 6, 9 and 12 are objected to because of informalities. By this Amendment, the objected to claims have been amended to clarify and address the informalities. Accordingly, it is respectfully submitted that these objections are moot.

IV. 35 U.S.C. §102 and §103 Rejections

Claims 1, 3-6, 8-10, 13 and 19 are rejected under 35 U.S.C. §102(b) over a first interpretation of Ishii (U.S. Patent No. 6,404,414); claims 1, 3, 5, 8, 9 and 19 are rejected under 35 U.S.C. §102(b) over a second, alternative interpretation of Ishii; and claims 7 and 11 are rejected under 35 U.S.C. §103(a) over Ishii in view of Murade (U.S. Patent Publication No. 2001/0022572). The rejections are respectively traversed.

Ishii fails to disclose an electro-optical device having capacitors having first electrodes with conductive layers connected to data lines without any switching element between the conductive layers and the data lines, and having second electrodes with other conductive layers connected to capacitor electrode wiring lines, as generally recited in claim 1 and as similarly recited in claim 6.

Ishii, under the first interpretation asserted in the Office Action, fails to disclose all of the features recited in independent claims 1 and 6 because data lines 30 do not extend above switching circuit 83 in Fig. 1 of Ishii, as asserted in the Office Action. The portions of the wiring lines that extend above switching circuit 83 are not part of data lines 30 because such an interpretation is contrary to the concept of a switch, which inherently separates data lines 30 from the lines connected to the other terminal of the switches in switching circuit 83. As stated in Ishii at col. 7, lines 45-51, switching circuit 83 is used as an "on/off" switch to selectively connect reset signal lines 81 and 82 to data lines 30. Therefore, the Office Action's interpretation that data lines 30 extend above the switch component of switching circuit 83 is inconsistent with the teaching and disclosure of Ishii.

Ishii, under the second interpretation in the Office Action, fails to disclose all of the features recited in independent claims 1 and 6 because the mere crossing of data lines 30 with capacitor lines 29 in a wiring schematic does not translate to the actual crossing of those wires in a real-world circuit, or in the creation of "inherent" capacitance, as asserted in the Office Action. Applicant submits, as stated above, that schematic wiring diagrams, such as that shown in Fig. 1 of Ishii, are representations of the arrangement and electrical connections of wires and elements, and a wiring schematic does not literally represent a real-world arrangement of actual wires and elements. It is thus submitted that the Office Action's assumption that data lines 30 actually cross capacitor lines 29 is not correct, and furthermore it cannot be assumed that data lines 30 and capacitor lines 29 are sufficiently close to result in an inherent capacitance, as asserted in the Office Action. Furthermore, there is no teaching in Ishii that such a capacitance is present and the Ishii device is not disclosed to operate with, or even account for, a capacitance between device wiring. As shown in Ishii in Fig. 1, capacitors are placed in the Ishii device where capacitance is desired (see, for example, capacitor 85 of Fig. 1), and there is no indication or suggestion that Ishii discloses two forms of capacitance, that which is expressly disclosed and that which is to be inferred from Ishii's specification.

To further clarify this matter, claim 1 has been amended to clarify that the claimed capacitors are being provided separately from any inherent capacitance of any switching element.

In view of the foregoing, Ishii by itself, and as modified by Murade, fails to disclose all of the features recited in independent claims 1 and 6, as well as the additional features recited in the dependent claims thereof. It is respectfully requested that the rejections be withdrawn.

V. Conclusion

In view of the foregoing, it is respectfully submitted that this application is in condition for allowance. Favorable reconsideration and prompt allowance are earnestly solicited.

Should the Examiner believe that anything further would be desirable in order to place this application in even better condition for allowance, the Examiner is invited to contact the undersigned at the telephone number set forth below.

Respectfully submitted,



James A. Oliff
Registration No. 27,075

Steven W. Allis
Registration No. 50,532

JAO:SWA/jam

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OLIFF & BERRIDGE, PLC
P.O. Box 19928
Alexandria, Virginia 22320
Telephone: (703) 836-6400

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